

# **Reliability of eWLB (embedded wafer level BGA) for Automotive Radar Applications**

by

**Daniel Yap, Kim Sing Wong, Luc Petit\*,  
Roberto Antonicelli\*\* and Seung Wook Yoon\*\*\***

**STMicroelectronics Pte Ltd. 629 Toa Payoh, Lorong 4, Singapore 319521**

**\*STMicroelectronics, 12 rue Jules Horowitz, B.P. 217, 38019 Cedex, Grenoble, France**

**\*\* STATS ChipPAC, Rue de Lausanne 47 1110, Morges, Switzerland**

**\*\*\*STATS ChipPAC Pte Ltd., 10 Ang Mo Kio Street 65 Techpoint #04-08/09, Singapore**

**Originally published by IEEE 67th Electronic Components and Technology Conference,  
Orlando, Florida, May 30 – June 2, 2017.**

**Copyright © 2017.**

**By choosing to view this document, you agree to all provisions of the  
copyright laws protecting it.**



**Advanced Wafer-Level Technology: eWLB/FO-WLP for mmWave & Automotive Radar Applications**

f - - w  
 ( ) f w w  
 3-5 . ▲ (▲ ) 77  
 6, 7, - - 60  
 f w f 94 140 f 8  
 f . T f f  
 f f 10 . T , f w  
 f f f f ,  
 ▲ f w f ( ) .  
 ( w- ) f 4350 f  
 1-40 9 77 - ( T ) T  
 10 .  
 ▲ w f .  
 11, 12 .  
 T f w f - f .  
 . T f



ff w f f  
T w T T (-40/125 , 2  
w / ). T f w w 4  
8- f 1.2 w  
w 250 f f N / w  
f w T w f 99% w  
w T T , f w  
T 3 f T f

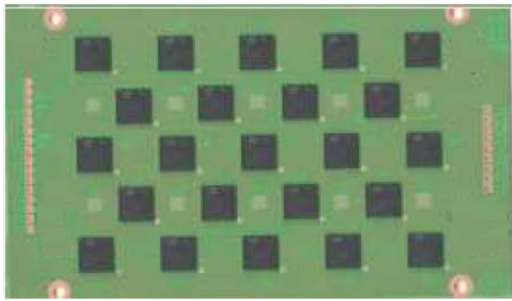


Figure 4. Micrograph of PCB for board level reliability with surface mounted eWLB packages.

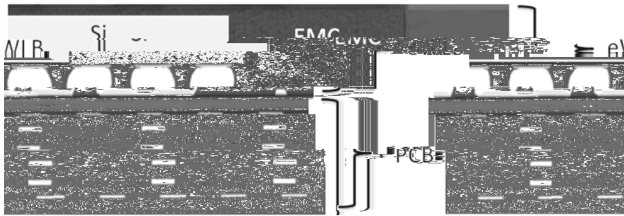


Figure 5. Micrograph of eWLB mounted PCB for board level reliability test.

- w:
1. NSMD board pads showed improved TCoB performance versus SMD ones in all DOE legs.
  2. Larger solder mask opening was effective to improve results for the NSMD pad.
  3. Thicker Cu RDL showed 15~20% improvement in TCoB test.
  4. UBM did not effectively improve TCoB in this study, but is leading to big change in failure distribution. Need to further investigate and understand this behavior and failure mechanism(s).
  5. With UBM and a new solder alloy B, it increased TCoB in NSMD pad.

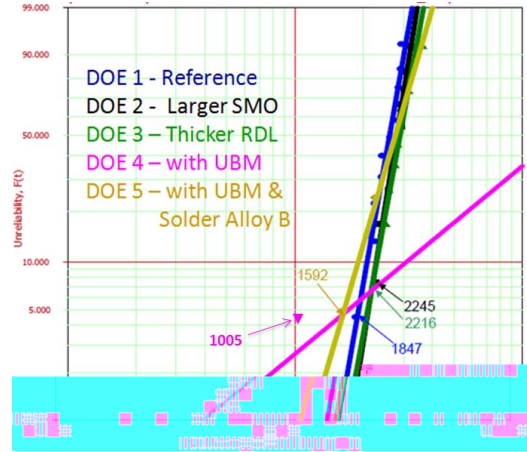


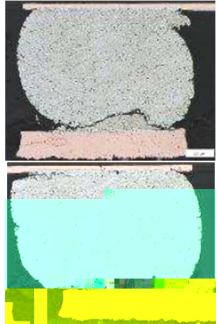
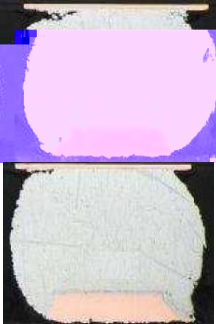
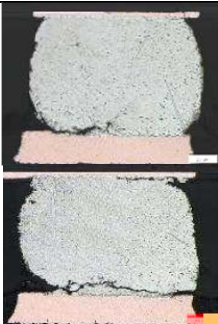
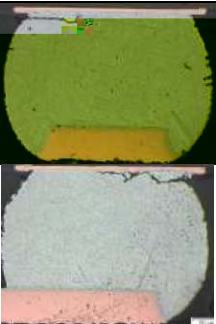
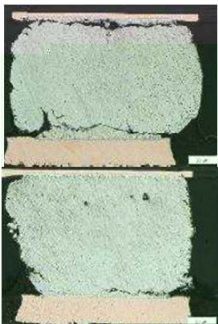


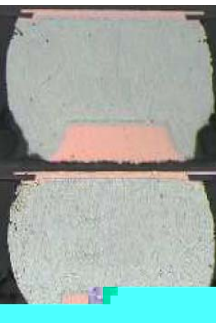
Figure 6. DOE study result : Weibull plot of TCoB reliability test with NSMD PCB. Each no. indicates the first failure cycle in each DOE studies.

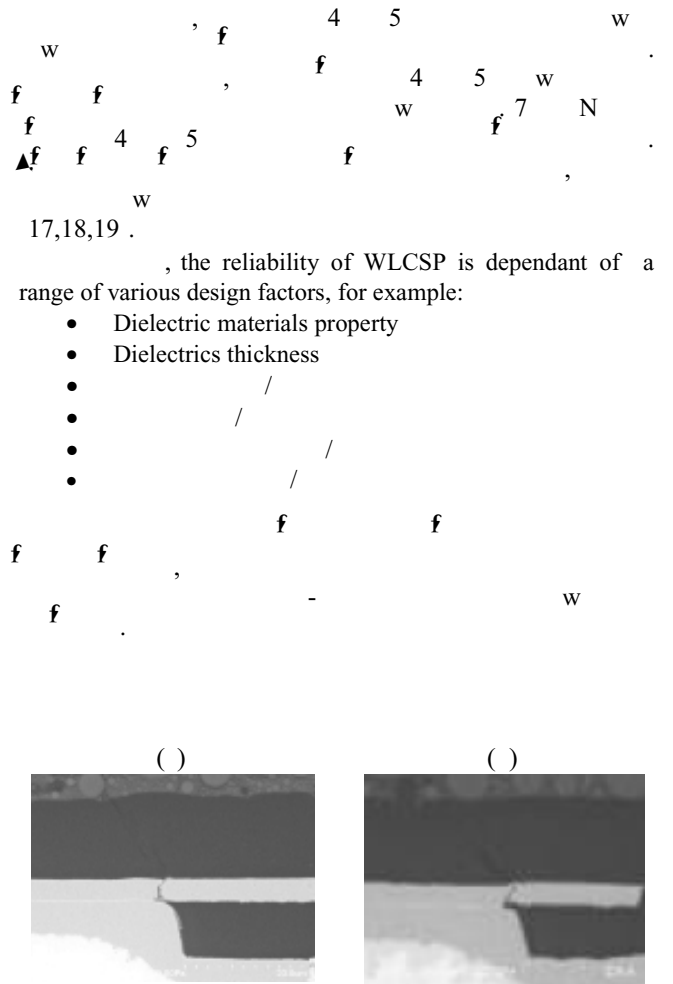
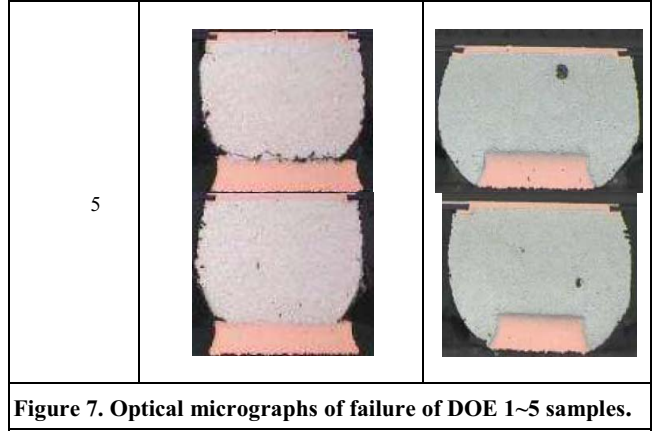
T 3. f T  
f T

		First Failure Cycles	
		SMD	NSMD
DOE1	Reference	1452	1847
DOE2	Larger solder mask opening	1388	2245
DOE3	Thicker RDL	1763	2216
DOE4	With UBM+RDL	964	1005
DOE5	With UBM and Solder Alloy B	1592	1847

Failure Analysis of Solder Joint Reliability

▲f f f w  
w f f w N 7,  
f f f w  
N w w f w  
f N w  
f T 2 w f w

	SMD	NSMD
1		
2		
3		
4		



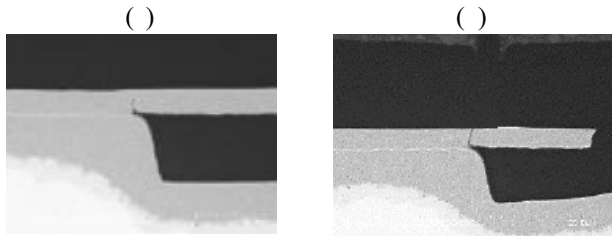


Figure 9. SEM micrographs of failure of DOE 5 (with UBM and solder alloy B & NSMD)

### III. Discussion

The failure of DOE 5 is characterized by the presence of a large, irregular void in the solder joint, as shown in the SEM micrographs. This void is likely caused by the presence of UBM and solder alloy B & NSMD. The failure is characterized by the presence of a large, irregular void in the solder joint, as shown in the SEM micrographs. This void is likely caused by the presence of UBM and solder alloy B & NSMD. The failure is characterized by the presence of a large, irregular void in the solder joint, as shown in the SEM micrographs. This void is likely caused by the presence of UBM and solder alloy B & NSMD.

### References

1. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 2016, <http://www.f...>
2. <http://www.f...>
3. <http://www.f...> 2001

4. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 29 (1999), 1999.
5. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 2000, 2000.
6. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 1037-1038, 2002.
7. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 76-77, 2001.
8. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 2005, 2005.
9. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 94, 2002.
10. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 1480-1491, 2000.
11. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 56, 2006.
12. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 8, 2006.
13. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 77, 2008.
14. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", 10.1016/. 2016.07.104
15. "Failure Analysis of DOE 5 (with UBM and solder alloy B & NSMD)", (2013)

16. ( " w f f ▲ (▲.) " ,  
 T 2016, (2016)  
 ://www. ▲
17. T , T T f  
 , f f' ▲  
 T " ,
18. ▲ T 2010, (2010)  
 ▲ , T T , N ,  
 ▲ , " f
19. , 50, 7, 2010,  
 928-936 (2010)  
 ▲ , T T , T ,  
 N , w, ,  
 f ' f " f ' ,  
 " f f T▲, 5 ▲  
 15-16, 2008 , ▲.(2008)